

FIG. 1

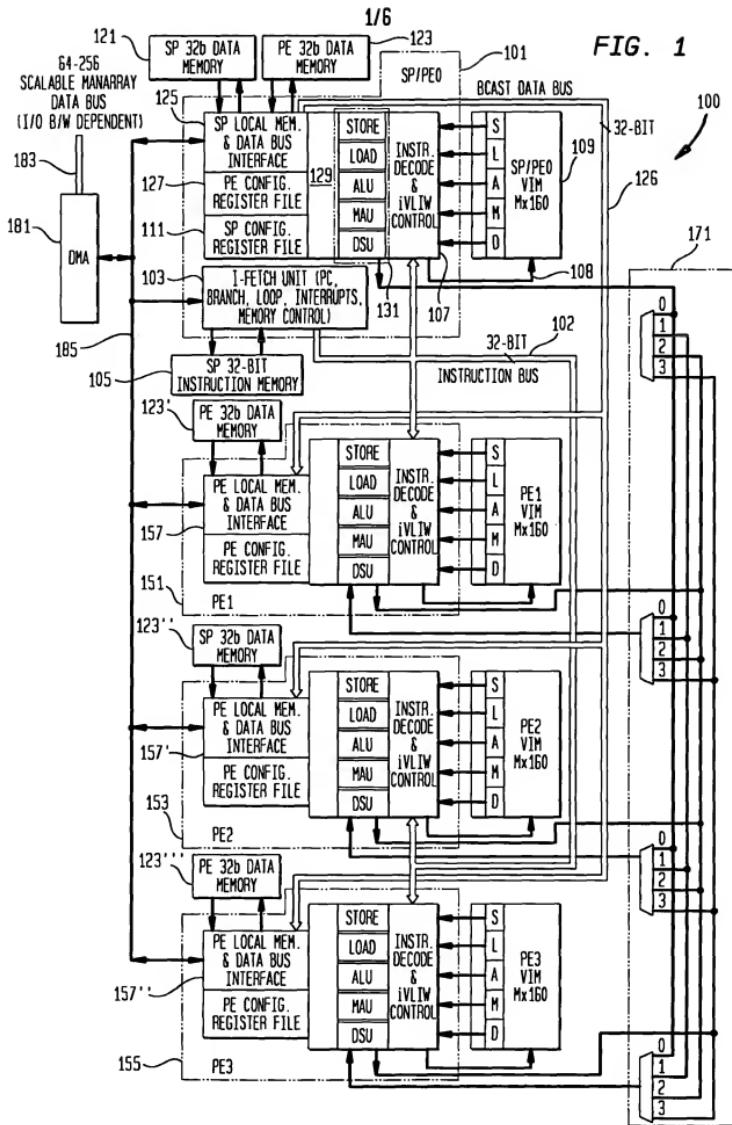


FIG. 2A

200

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Group	SIP	Unit	Allopcode																												
			CPIcc																												

FIG. 2B

202

CC	Description	C-N-V-Z Setting
Z(EQ)	Zero or Equal	Z=1
NZ(NE)	Not Zero or Not Equal	Z=0
H1	Higher (unsigned)	(C=1) && (Z=0)
HS(CS)	Higher or Same (unsigned), or Carry Set	C=1
LO(CC)	Lower (unsigned), or Carry Clear	C=0
LS	Lower or Same (unsigned)	(C=0)    (Z=1)
VS	Overflow Set	V=1
VC	Overflow Clear	V=0
POS	Positive	(N=0) && (Z=0)
NEG	Negative	N=1
GE	Greater-than or Equal (signed)	N=V
GT	Greater-than (signed)	(Z=0) && (N>V)
LE	Less-than or Equal (signed)	(Z=1)    (N<V)
LT	Less-than (signed)	N!=V

204

205

206

207

208

FIG. 3A

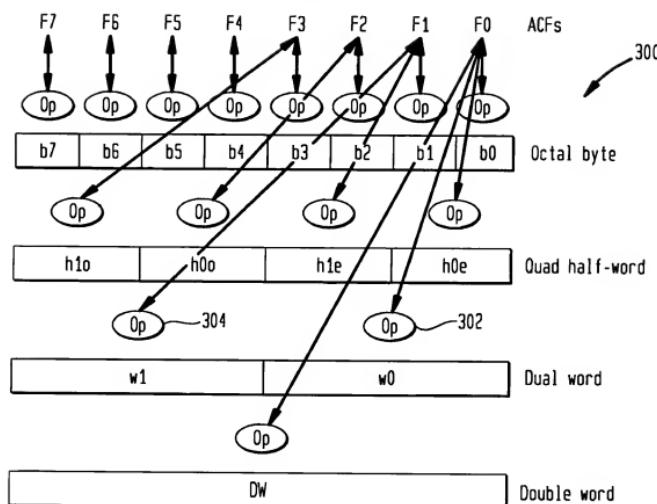


FIG. 3B

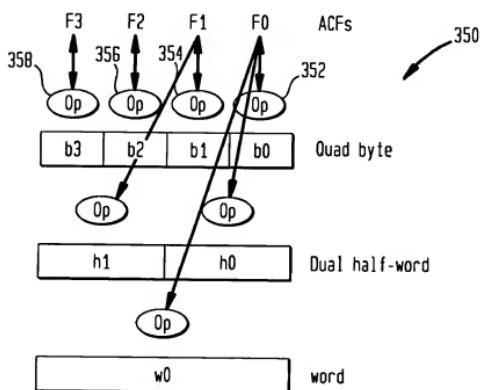


FIG. 4A

Load Direct Instruction Encoding									
31	30	29	28	27	26	25	24	23	22
Group	S/P	Size	0	Anode	CE1	RT 5-0			L/S
									WADDR15

400

402

FIG. 4B

Copy Instruction Encoding									
31	30	29	28	27	26	25	24	23	22
Group	S/P	Size	0	DestOpCode	RT 4-0				410
									RT 5
									Rx 5
									CopyExt
									CE2

412

420

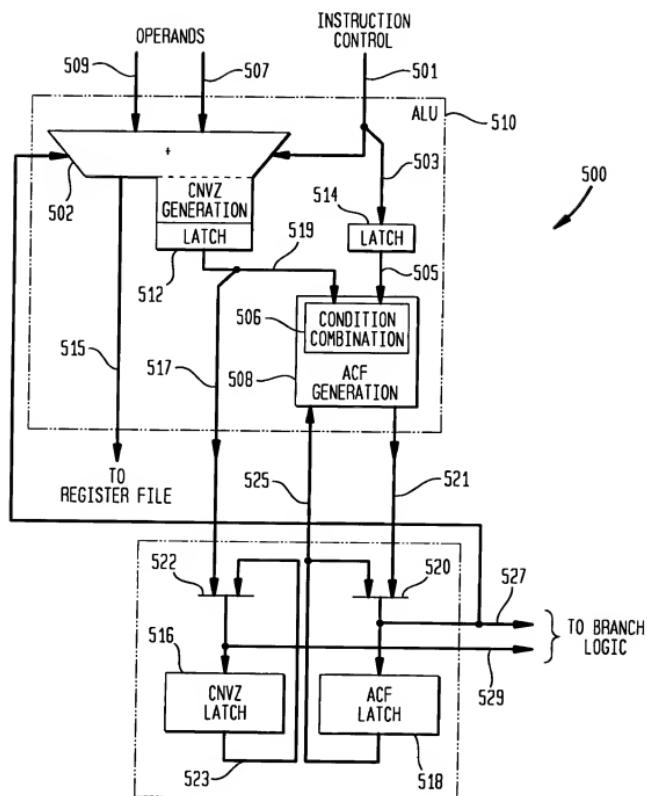
FIG. 4C

Logic Instruction Encoding									
31	30	29	28	27	26	25	24	23	22
Group	S/P	Unit	AllOpCode	RT					422
									RT
									Ry
									Rz
									LogicExt
									CE2b
									CE2a

420

422

FIG. 5A



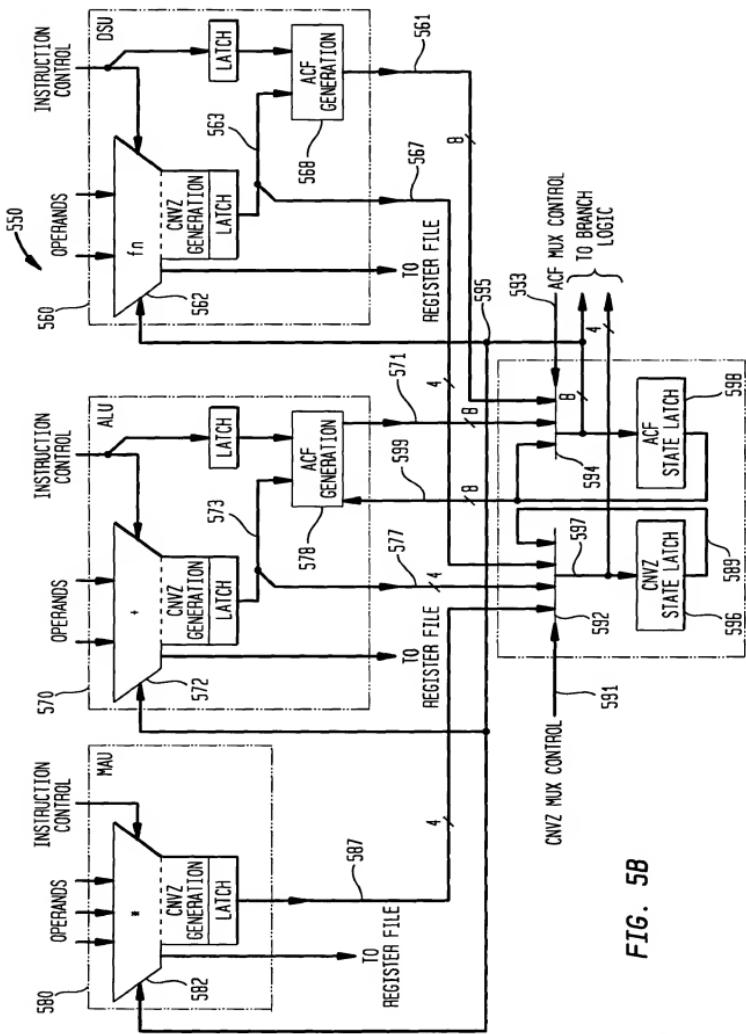


FIG. 5B